Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.(Original) A method of connecting a differential inverter circuit to a voltage supply and to a ground, comprising:

applying a first differential voltage component of a differential input voltage to a first input transistor and a second differential voltage component of the differential input voltage to a second input transistor;

connecting a first current source between the first input transistor and the voltage supply and a second current source between the second input transistor and the voltage supply;

connecting a third current source between the first and second input transistors and the ground;

coupling a first load device in parallel with the first input transistor between the first and third current sources and generating an inverted second differential voltage component at a connection between the first current source and the first input transistor; and

coupling a second load device in parallel with the second input transistor between the second and third current sources and generating an inverted first differential voltage component at a connection between the second current source and second input transistor.

2.(currently amended) The method of Claim 1, wherein the first load device [[transistor]] and the second load device [[transistor]] are each a diode-connected transistor.

3.(currently amended) The method of Claim 1, wherein the first load device [[transistor]] and the second load device [[transistor]] are each a folded load transistor.

4.(original) The method of Claim 1, wherein the third current source operatively generates a current that is about twice as large as a current operatively generated by the first current source or second current source.

5.(original) The method of Claim 1, further comprising comparing a common-mode voltage of the inverted first differential output voltage component and the inverted second differential output voltage component to a reference voltage signal generated by a reference voltage circuit and for generating a feedback signal based upon the comparison.

6.(original) The method of Claim 5, wherein:

at least one of the first current source, second current source and third current source includes a control input for receiving the feedback signal; and

the at least one of the first current source, second current source and third current source generates a current that is responsive to the feedback signal.

7.(original) The method of Claim 1, wherein the first input transistor and the second input transistor are each a MOSFET.

8.(original) The method of Claim 1, wherein the first load transistor and the second load transistor are each a MOSFET.

9.(original) The method of Claim 1, wherein the first current source, second current source and third current source are each a MOSFET.

10.(original) A method of connecting a differential inverter circuit between a first power supply and a second power supply, comprising:

coupling first and second current sources to the first power supply;

coupling a third current source to the second power supply;

generating first, second and third currents from the respective first, second and third current sources;

sinking a current from the first current source to a first input transistor of the differential inverter circuit;

sinking a current from the first current source to a first load transistor of the differential inverter circuit, the first load transistor being connected in parallel to the first input transistor;

sinking a current from the second current source to a second input transistor of the differential inverter circuit;

sinking a current from the second current source to a second load transistor, the second load transistor being connected in parallel to the second input transistor; and

sinking a plurality of currents from the third current source to each of the plurality of first and second input and load transistors.

11. (currently amended) The method of claim 10, wherein the first power supply [[terminal]] is a voltage supply [[terminal]] and the second power supply [[terminal]] is a ground [[terminal]].